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Sp601 Hardware User Guide o Hardware User Guide – Detailed description of the SP601 Board features, jumper settings, options and pin location tables. o Reference Design User Guide – Step by Step guide on understanding, implementing, modifying and customizing the SP601 Reference Designs. o Board Design Files – Schematics/Gerbers/BOM • Cables & Power Supply.

Spartan-6 FPGA SP601 Evaluation Kit FAQ SP601 Setup. Connect a USB Mini-B Cable to the USB JTAG connector on the SP601 board. – Connect this cable to your PC. Compile MultiBoot Design. Unzip the rdf0006.zip file. Note: Presentation applies to the SP601. Compile MultiBoot Design. At a command prompt, type: cd sp601_multiboot run_all_build_script.bat.

XTP038: SP601 MultiBoot Design - Xilinx Hardware User ' s Manual NJ/NX-series CPU Unit Software User ' s Manual NJ/NX-series Instructions Reference Manual NJ/NX-series CPU Unit Motion Control User's Manual NJ/NX-series Motion Control Instructions Reference Manual NJ/NX-series CPU Unit Built-in EtherCA T Port User ' s Manual NJ/NX-series CPU Unit Built-in EtherNet/IP Port User ...

NX/NY-series Artificial Intelligence Machine Automation ... g n i k o l CAGP F 6 - n a t r a p † S Resources User Guide. Preface: About This Guide 8 www.xilinx.com SP605 Hardware User Guide UG526 (v1.9) February 14, 2019 This guide describes the clocking resources available in all Spartan-6 devices, including the DCMs and PLLs. SP605 Hardware User Guide - Xilinx

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CiteSeerX — SP601 Reference Design User Guide SP601 BIST Design Description. Description. – The Built-In System Test (BIST) application uses an EDK MicroBlaze system to verify board functionality. A UART based terminal program interface offers users a menu of tests to run. Reference Design IP.

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